

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Complete if Known			
Application.Number	10/611,621		
Filing Date June 30, 2003			
First Named Inventor	Peleg, Alexander		
Group Art Unit	2193		
Examiner Name	Malzahn, David		
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Sheet 1 of 7	Attorney Docket No: 42P1576

	US PATENT DOCUMENTS Pages, Columns, Lines,					
Examiner initial *	Cite No 1	USP Document Number	Publication or Issue Date MM-DD-YYYY	Name of Patentee or Applicant of cited Document	Where Relevant Passages or Relevant Figures Appear	
	-	US-3,641,736	02/15/1972	Dwyer, Raymond J.		
178		US-3,675,001	07/04/1972	Singh, Shanker		
		US-3,711,692	01/16/1973	Batcher, Kenneth E.		
•		US-3,723,715	03/27/1973	Chen, Tien C., et al.		
		US-3,900,724	08/19/1975	McIver, George W., et al.		
		US-3,941,990	03/02/1976	Rabasse, Jean		
		US-4,161,784	07/17/1979	Cushing, David E., et al.		
		US-4,344,151	08/10/1982	White, Stanley A.		
		US-4,393,468	07/12/1983	New, Bernard J.		
		US-4,418,383	11/29/1983	Doyle, Donald E., et al.		
		US-4,498,177	02/05/1985	Larson, Brian R.		
		US-4,542,476	09/17/1985	Nagafuji, Motonobu		
		US-4,677,582	06/30/1987	Nagafuji, Motonobu		
		US-4,707,800	11/17/1987	Montrone, Dale L., et al.		
		US-4,760,545	07/26/1988	Inagami, Yasuhiro , et al.		
		US-4,771,379	09/13/1988	Ando, Hideki , et al.		
		US-4,811,269	03/07/1989	Hirose, Kenji , et al.		
		US-4,876,660	10/24/1989	Owen, Robert E., et al.		
4		US-4,890,218	12/26/1998	Bram, Jeffry M.		
		US-4,945,479	07/31/1990	Rusterholz, John T., et al.		
		US-4,956,801	09/11/1990	Priem, Curtis, et al.		
		US-4,972,362	11/20/1990	Elkind, Bob, et al.		
		US-4,985,848	01/15/1991	Pfeiffer, David M., et al.		
		US-4,989,168	01/29/1991	Kuroda, Koji , et al.		
		US-5,001,662	03/19/1991	Baum, Allen J.		
		US-5,032,865	07/16/1991	Schlunt, Robert S.		
		US-5,047,975	09/10/1991	Patti, Michael F., et al.		
		US-5,081,698	01/14/1992	Kohn, Leslie D.		
		US-5,095,457	03/10/1992	Jeong, Ho-Sun		
		US-5,111,422	05/05/1992	Ullrich, Manfred F.	· · · · · · · · · · · · · · · · · · ·	
		US-5,126,964	06/30/1992	Zurawski, John H.		
		US-5,161,247	11/30/1992	Murakami, Tokumichi , et al.		
		US-5,187,679	02/16/1993	Vassiliadis, Stamatis, et al.		
		US-5,189,636	02/23/1993	Patti, Michael F., et al.		
		US-5,193,167	03/09/1993	Sites, Richard L., et al.		
		US-5,210,711	05/11/1993	Rossmere, David, et al.		
		US-5,222,037	06/22/1993	Taniguchi, Takashi		
		US-5,227,994	07/13/1993	Ohki, Mitsuharu		

EXAMINER

D. H. Malzahn

DATE CONSIDERED 7/5/07

Substitute for form 1449A/PTO

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Attorney Docket No: 42P15769 Sheet 2 of 7

83		US-5,241,492	08/31/1993	GIRARDEAU, JR., , et al.	1
	<u> </u>	US-5,241,493	08/31/1993	Chu, Tan V., et al.	
		US-5,262,976	11/16/1993	Young, William R., et al.	
		US-5,268,995	12/07/1993	Diefendorff, Keith E., et al.	
		US-5,293,558	03/08/1994	Narita, Masahisa , et al.	
		US-5,303,355	04/12/1994	Gergen, Joseph P., et al.	
		US-5,311,508	05/10/1994	Buda, Paul R., et al.	
		US-5,321,644	06/14/1994	Schibinger, Joseph S.	
		US-5,325,320	06/28/1994	Chiu, Chiao-Er A.	
		US-5,327,369	07/05/1994	Ashkenazi, Yaron	
		US-5,327,543	07/05/1994	Miura, Masaharu , et al.	
		US-5,390,135	02/14/1995	Lee, Ruby B., et al.	
		US-5,420,815	05/30/1995	Nix, Michael A., et al.	
		US-5,426,598	06/20/1995	Hagihara, Yasuhiko	
		US-5,426,783	06/20/1995	Norrie, Chris, et al.	
		US-5,442,799	08/15/1995	Murakami, Tokumichi, et al.	
		US-5,457,805	10/10/1995	Nakamura, Kimiko	
		US-5,487,022	01/23/1996	Simpson, Richard, et al.	
		US-5,487,159	01/23/1996	Byers, Larry L., et al.	
		US-5,500,811	03/19/1996	Corry, Alan G.	
		US-5,506,865	04/09/1996	Weaver Jr., Lindsay A.	
		US-5,509,129	04/16/1996	Guttag, Karl M., et al.	
		US-5,515,520	05/07/1996	Hatta, Koichi, et al.	
		US-5,517,438	05/14/1996	Dao-Trong, Son, et al.	
		US-5,528,529	06/18/1996	Seal, David J.	
		US-5,541,865	07/30/1996	Ashkenazi, Yaron	
		US-5,576,983	11/19/1996	Shiokawa, Toshimichi	
		US-5,579,253	11/26/1996	Lee, Ruby B., et al.	
		US-5,590,365	12/31/1996	Ide, Nobuhiro, et al.	
		US-5,594,679	01/14/1997	lwata, Eiji	
		US-5,606,677	02/25/1997	Balmer, Keith, et al.	
		US-5,642,306	06/24/1997	Mennemeier, Larry M., et al.	
		US-5,651,121	07/22/1997	Davies, Daniel	
		US-5,673,427	09/30/1997	Brown, Gary L., et al.	
		US-5,701,508	12/13/1997	Glew, Andrew F., et al.	
		US-5,719,642	02/17/1998	Lee, Chen-Yi	
		US-5,721,697	02/24/1998	Lee, Ruby B.	
		US-5,721,892	02/24/1998	Peleg, Alexander , et al.	
		US-5,737,537	04/07/1998	Gardos, Thomas R., et al.	
		US-5,740,037	04/14/1998	McCann, Thomas E., et al.	*
لنسا		US-5,742,529	04/21/1998	Mennemeier, Larry M., et al.	
4		US-5,742,840	04/21/1998	Hansen, Craig , et al.	
-	•				

EXAMINER

DATE CONSIDERED

Substitute for form 1449A/PTO

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Attorney Docket No: 42P15769

		_ ,		
	US-5,765,037	06/09/1998	Morrison, Gordon E., et al.	
7	US-5,778,419	07/07/1998	Hansen, Craig C., et al.	
	US-5,790,208	08/04/1998	Kwak, Jin S., et al.	
	US-5,793,661	08/11/1998	Dulong, Carole, et al.	
	US-5,793,900	08/11/1998	Nourbakhsh, Illah R., et al.	
	US-5,794,060	08/11/1998	Hansen, Craig, et al.	·
	US-5,794,061	08/11/1998	Hansen, Craig, et al.	
	US-5,802,336	09/01/1998	Peleg, Alexander, et al.	
	US-5,805,913	09/08/1998	Guttag, Karl M., et al.	
	US-5,806,049	09/08/1998	Petruzzi, Christopher R.	
	US-5,809,321	09/15/1998	Hansen, Craig, et al.	
	US-5,819,101	10/06/1998	Peleg, Alexander, et al.	<u> </u>
	US-5,822,603	10/13/1998	Hansen, Craig, et al.	
	US-5,835,392	11/10/1998	Dulong, Carole, et al.	
	US-5,841,676	11/24/1998	Ali, Fatma O., et al.	
	US-5,852,473	12/22/1998	Horne, Caspar, et al.	
	US-5,852,726	12/22/1998	Lin, Derrick, et al.	
	US-5,865,752	02/02/1999	Seyed-Bolorforosh, Mir S., et al.	
	US-5,870,619	02/09/1999	Wilkinson, Paul A., et al.	
	US-5,876,342	03/02/1999	Chen, Jian-Feng, et al.	
	US-5,880,979	03/09/1999	Mennemeier, Larry M., et al.	
	US-5,880,983	03/09/1999	Elliott, Timothy A., et al.	
	US-5,883,824	03/16/1999	Lee, Ruby B., et al.	
	US-5,884,071	03/16/1999.	Kosaraju, Chakravarthy	
	US-5,884,089	03/16/1999	Orian, Effi, et al.	
	US-5,907,842	05/25/1999	Mennemeier, Larry, et al.	
	US-5,909,552	06/01/1999	Jensen, Allen P., et al.	
	US-5,936,872	08/10/1999	Fischer, Stephen A., et al.	
	US-5,938,756	08/17/1999	Van Hook, Timothy J., et al.	
	US-5,946,405	08/31/1999	Kim, Jin W., et al.	
	US-5,953,241	09/14/1999	Hansen, Craig C., et al.	
	US-5,983,257	11/09/1999	Dulong, Carole	
	US-6,006,318	12/21/1999	Hansen, Craig C., et al.	
	US-6,018,351	01/25/2000	Mennemeier, Larry M., et al.	
	US-6,026,483	02/15/2000	Oberman, Stuart F., et al.	
	US-6,035,316	03/07/2000	Peleg, Alexander , et al.	
	US-6,035,387	03/07/2000	Hsu, Chia-Chang, et al.	
- -	US-6,041,403	03/21/2000	Parker, Donald D., et al.	
	US-6,058,408	05/02/2000	Fischer, Stephen A., et al.	-
	US-6,122,725	09/19/2000	Roussel, Patrice, et al.	
1.				
	US-6,178,482			1
	US-6,154,831	11/28/2000 01/23/2001	Thayer, John S., et al. Sollars, Donald L.	

EXAMINER)

D. H. Malzah

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2	US-6,192,467	02/20/2001	Abdallah, Mohammad A., et al.	
- W	US-6,230,253	05/08/2001	Roussel, Patrice , et al.	
	US-6,230,257	05/08/2001	Roussel, Patrice, et al.	
	US-6,233,671	05/15/2001	Abdallah, Mohammad, et al.	
	US-6,237,016	05/22/2001	Fischer, Stephen A., et al.	
	US-6,260,137	07/10/2001	Flech, Rod G., et al.	
	US-6,295,599	09/25/2001	Hansen, Craig, et al.	
	US-6,378,060	04/23/2002	Hansen, Craig, et al.	
	US-6,470,370	10/22/2002	Fischer, Stephen A., et al.	
	US-6,502,117	12/31/2002	Golliver, Roger A., et al.	
	US-6,584,482	06/24/2003	Hansen, Craig C., et al.	
	US-6,643,765	11/04/2003	Hansen, Craig , et al.	
	US-6,725,356	04/20/2004	Hansen, Craig, et al.	

	FOREIGN PATENT DOCUMENTS					
Examiner Initials*	Cite No 1	Foreign Patent Document Country Code/Number/Kind Code (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T²
		DE-19914617	10/14/1999	Roussel, Patrice, et al.		1
		DE-3636106	04/23/1987	Hideki, Ando, et al.		
		EP-0308124	03/22/1989	Pfeiffer, David M., et al.		
		GB-2339040	01/12/2000	Roussel, Patrice, et al.		
		JP-3268024	03/19/1990	Masamichi, Fukaya, et al.		
		JP-6266554	09/22/1994	Katsunori, Suzuki , et al.		
		WO-9708608	03/06/1997	Peleg, Alexander D., et al.		
		WO-9722921	06/27/1997	Lin, Derrick, et al.		
		WO-9722923	06/29/1997	Orenstein, Doron, et al.		\top
		WO-9722924	06/29/1997	Glew, Andrew F., et al.		\top
	Γ	WO-9723821	07/03/1997	Dulong, Carole, et al.	<u> </u>	\top
		WO-9950740	10/07/1999	Rousell, Patrice, et al.		1

		. 01	THER DOCUMENTS NON PATENT LITERATURE DOCUMENTS	
	miner ials*	Cite No 1	include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
			"EP96930495", Search Report, (11/18/1999), 2 pages	
			"MIPS Digital Media Extension", C1-C40 , J 9 9 6	
			"MIPS V Instruction Set", B1-B7 , J 9 9 6	
			"PCT/US96/12799", Search Report, (11/26/1996),1	
			10/610831, "Final Office Action", (2/28/07),1-7	
	. 4		10/610831, "Office Action", (8/4/06), 1-7	
#			ABBOTT, CURTIS, et al., "Broadband Algorithms with the MicroUnity Mediaprocessor", Proceedings of COMPCON '96, (1996),349-354	

EXAMINER D. H. Malzah DATE CONSIDERED 7/5/07

Substitute for form 1449A/PTO	Complete if Known			
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	70	HER DOCUMENTS NON PATENT LITERATURE DOCUMENTS	
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200		ADVANCED MICRO DEVICES, "AMD-3D Technology Manual", (Feb. 1998),	
1 1		BIPOLAR INTEGRATED TECHNOLOGY, "BIT Preliminary", B3110/B3120;	
 - - 		B2110/B2120 Floating Point Chip Set, 1-40 , 1987	
		CASE, BRIAN, "Philips Hopes to Displace DSPs with VLIW", Microprocessor Report, (Dec. 1994),12-18	
		COMPLAINT, "In the United States District Court for the District of Delaware", In the	
		matter of Transmeta v. Intel Corporation, (10/11/2006),1-7	
		ELKIND, BOB, et al., "A SUB 10 nS Bipolar 64 Bit Integrated/Floating Point	1
		Processor Implemented on Two Circuits", IEEE 1987 BCTM, (198),101-104] {
		FARMWALD, PAUL M., "High Bandwidth Evaluation of Elementary Functions, S-1	
		Project", IEEE, (1981),139-142	1
		FARMWALD, P. M., "On the Design of High Performance Digital Arithmetic Units",	
		<u>UCRL-53190, (Aug. 1981),i-viii & 1-95</u>	
		FIRST AMENDED COMPLAINT, "In the United States District Court for the District of	
		Delaware", In the matter of Transmeta Corporation v. Intel Corporation, C.A. No, 06-	
		663 (GMS),(12/12/2006),1-8	\vdash
		GRIMES, JACK, et al., "64-Bit Processor. The Intel i860 64-Bit Processor: A General	
		Purpose CPU with 3D Graphics Capabilities", (July 1989),85-94 GWENNAP, LINLEY, "New PA-RISC Processor Decodes MPEG Video",	\vdash
		Microprocessor Report, (Jan. 1994),16-17	i l
		HANSEN, CRAIG, "Architecture of a Broadband Mediaprocessor", MicroUnity	
		Systems Engineering, Inc., (1996),334-354	
		HAYES, RAYMOND, et al., "MicroUnity Software Development Environment",	
		Proceedings of COMPCON '96, (1996),341-348	
		HEWLETT PACKARD, "64-Bit and Multimedia Extensions in the PA-RISC 2.0	
		Architecture", rblee@cup.hp.com.huck@cup.hp.com, 1-8, 1997	
		IBM, "TDB: Double Speed, Single Precision Vector Register Organization Using Double Port Chips", (Feb. 1981),1-6	
		IDE, NOBUHIRO, et al., "A 320-MFLOPS CMOS Floating-Point Processing Unit for	
		Superscalar Processors", <u>IEEE Journal of Solid State Circuits</u> , Vol. 28, No. 3,(March 1993),352-361	
		IINO, HIDEYUKI, et al., "ISSCC 92 Session 6 / Microprocessors / TA 6.5: A	
		289MFLOPS Single-Chip Supercomputer", 1992 IEEE International Solid State	
		Circuits Conference, (1992),112-113	<u> </u>
		INTEL CORPORATION, "i860 Microprocessor Family Programmer's Reference	
		Manual", (1992),Ch. 1, 3, 8 & 12	
		INTEL CORPORATION, "Pentium Processor User's Manual", Volume 3: Architecture	
1		and Programming Manual, (1993),Ch. 1, 3-4, 6, 8 & 18 INTEL CORPORATION'S ANSWER, AFFI, "In the United States District Court for the	
FRY		District of Delaware", In the matter of Transmeta Corporation v. Intel Corporation, Civil	
		Signal of Science , in the matter of manameta corporation 4, inter-corporation, Civil	

EXAMINER D. H. Malzaka DATE CONSIDERED 7/5/07

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		Action No. 06-663-GMS,(1/9/2007),1-27	
7		KAWAKAMI, YUICHI, et al., "LSI Applications: A Single-Chip Digital Signal Processor	
1 1 1		for Voiceband Applications", Solid State Circuits Conference, Digest of Technical	
		Papers, IEEE International, (1980),	
		KILLIAN, EARL, "MIPS Extension for Digital Media", Silicon Graphics, Inc., 1-10,1994	
		KOHN, LES, et al., "Introducing the Intel i860 64 Bit Microprocessor", Intel	
		<u>Corporation</u> , (Aug. 1989),15-30	
1 1 1		LEE, RUBY B., "Accelerating Multimedia with Enhanced Microprocessors", Hewlett	
		Packard. IEEE Micro, (April 1995),22-32	
1 1		LEE, RUBY B., "Realtime MPEG Video via Software Decompression on a PA-RISC	i l
		Processor", 1995 IEEE, (1995),186-192	
]		LEIBOWITZ, BOB, et al., "System Design: ECL gains ground in battle against	
		CMOS", Computer Design, (4/1/1987),91-95	
1 1		LEVINTHAL, ADAM, et al., "Chap-A SIMD Graphics Processor", Computer Graphics	
		Project, Lucasfilm Ltd., (1984),77-82	
1 1 1		LEVINTHAL, ADAM, et al., "Parallel Computers for Graphics Applications", Pixar,	
		San Rafael, CA, (1987),193-198	
1 1 1		MANFERDELLI, JOHN L., "Signal Processing Aspects of the S-1 Multiprocessor	
		Project", <u>UCRL-84658,</u> (July 28, 1980),1-8	
		MARGULIS, NEAL, "i860 Microprocessor Architecture", McGraw Hill, Inc.,	1 1
		(1990),Ch. 6-8 & 10-11	
1 1 1		MIPS TECHNOLOGIES, INC,, "MIPS Extension for Digital Media with 3D",	1 1
		(3/12/1997),1-26	
		MOTOROLA, INC., "Errata to MC88110 Second Generation RISC Microprocessor	'
		User's Manual", (1992),1-11	
		MOTOROLA, INC., "MC88110 Programmer's Reference Guide", (1992),1-4	
		MOTOROLA, INC., "MC88110 Second Generation RISC Microprocessor User's	li
		Manual", (1991),	$oxed{oxed}$
		PHILIPS ELECTRONICS, "TriMedia TM1000 Preliminary Data Book", (1997),	
		SAMSUNG ELECTRONICS, "21164 Alpha Microprocessor Data Sheet", (1997),	
		SANTORO, MARK R., "Design and Clocking of VLSI Multipliers", Technical Report	
		No. CSL-TR-89-397, (Oct. 1989),i-xiii and 1-118	
		SANTORO, MARK, et al., "Session II: High-Speed Microprocessor", A Pipelined	
		64X64b Interactive Array Multiplier, IEEE International Solid State Circuits	
		Conference,(36-37 & 290),1988	
`		SANTORO, MARK R., et al., "SPIM: A Pipelined 64x64 bit Interactive Multiplier", IEEE]
		Journal of Solid-State Circuits, Vol. 24, No. 2, (Apr. 1989),487-493	
		SHIPNES, J., "Graphics Processing with the 88110 RISC Microprocessor", IEEE,	
- 		(1992),	
102		SILICON GRAPHICS, "Silicon Graphics Introduces Enhanced MIPS Architecture to	

EXAMINER DH. Malzahn DATE CONSIDERED 0/5/07

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936		Lead the Interactive Digital Revolution", (10/21/1996),2	
1 14		SILICON GRAPHICS, INC., "Silicon Graphics Introduces Compact MIPS@ RISC	
		Microprocessor Code for High Performance at a Low Cost", www.sgi.com,	
		(10/21/1996),1-2	
	i	SPADERNA, D., et al., "An Integrated Floating Point Vector Processor for DSP and	
		Scientific Computing", SHARP Microelectrinics Technology, Inc. IEEE, (1989),8-13	
	•	SUN MICROSYSTEMS, INC., "Real-Time MPEG2 Decode with the Visual Instrution	
		Set (VIS)", ULTRASPARC and New Media Support, (1996),1-8	
		SUN MICROSYSTEMS, INC., "The ULTRASPARC Processor - Technology White	ļ
		Paper", (1995),35 pages	
		SUN MICROSYSTEMS, INC., "The VIS Instruction Set", (1997),1-2 SUN MICROSYSTEMS, INC., "The Visual Instruction Set (VIS): On Chip Support for	
		SUN MICROSYSTEMS, INC., "The Visual Instruction Set (VIS): On Chip Support for	
		New-Media Processing", <u>ULTRASPARC</u> , (1996),1-7	
	,	SUN MICROSYSTEMS, INC., "Turbocharges Network Operations on New Media	
		Computing", ULTRASPARC, (1996),1-5	
		SUN MICROSYSTEMS, INC., "Ultra Port Archictecture (UPA): The New-Media	
	•	System Architecture", <u>ULTRASPARC</u> , (1996),1-4	ļ
		SUN MICROSYSTEMS, INC., "UltraSPARC Multimedia Capabilities On-Chip Support	
		for Real-Time Video and Advanced Graphics", SPARC Technology Business, (Sept.	1
		1994),	<u>l</u>
		SUN MICROSYSTEMS, INC., "Visual Instruction Set (VIS)", User's Guide, Version	
_		1.1, (March 1997),41-43 & 87-88	
		SWARTZLANDER JR., EARL, et al., "Proceedings: 11th Symposium on Computer	
		Arithmetic", Windsor Ontario, (6/29-6/2/1993),171-178	
	4)	TEXAS INSTRUMENTS, "TMS320C2X User's Guide", (1993),3:2-3:33; 3:28-3:34;	
		4:1-4-22; 4:41; 4:103; 4:119-4:120; 4:150-4:151	ĺ
		TRANSMETA, "Transmeta Corporation's Reply to Intel Corporation's Counterclaims	
		to the First Amended Complaint", (2/28/2007),1-20	
		UNDY, STEVE, et al., "A Low-Cost Graphics and Multimedia Workstation Chip Set",	
		<u>IEEE Micro</u> , (1994),10-22	
		WANG, YULUN, et al., "A Processor Architecture for 3D Graphics", IEEE Computer	
	L	Graphics & Applications, (Sept. 1992),96-105	
		WANG, YULAN, et al., "A Processor Architecture for 3D Graphics Calculations",	Ī
		Computer Motion, Inc., 1-23, 1992	1
axX.		WANG, YULUN, et al., "The 3DP: A Processor Architecture for Three-Dimensional	
STAN STAN		Applications", IEEE, (Jan 1992),25-36	

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